IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2811

Examiner: To Be Assigned

Re PASENT APPLICATION of:

Applicant:

NOV 1 5 2004

Kiyonori WATANABE

Serial No.:

10/697,318

Filed:

October 31, 2003

INFORMATION DISCLOSURE STATEMENT

For:

SEMICONDUCTOR DEVICE WITH IMPROVED

DESIGN FREEDOM OF EXTERNAL TERMINAL

Atty Ref.:

OHG 144

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an Information Disclosure Statement submitted in compliance with the timing requirements of 37 C.F.R. § 1.97(b)(3), i.e., prior to a First Office Action on the merits.

Attached is a copy of one Japanese patent publication, which was cited in the related co-pending U.S. patent application No. 10/697,015. Any relevance of the publication may be gleaned from the related co-pending application and from the attached English-language Abstract. The publication is listed on the attached Form PTO-1449. Consideration of the listed document is respectfully requested. The Examiner may also wish to consider related co-pending applications No. 10/697,247 and No. 10/722,446.

Since this Information Disclosure Statement is being filed before a first office action on the merits, no fee or certification is required, and the requirements of 37 C.F.R. §§ 1.97 and 1.98 are deemed to be fully met as to all listed document.

Should any fees be required, please charge the same to our deposit account No. 18-0002, and notify us accordingly.

November 15, 2004

Date

Robert H. Berdo, Jr. (Reg. No. 38, 075)

RABIN & BERDO, P.C. CUSTOMER NO. 23995 Telephone: (202) 371-8976 Facsimile: (202) 408-0924

Respectfully submitted,

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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty. Docket	Application	Application No.		
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				Applicant Kiyonori WATANABE				
				Filing Date October 31, 2003	Group 281	Group 2811		
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